



2822  
Docket No. 0756-1551  
JW M4  
KS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of ) Art Unit: 2822  
Shunpei YAMAZAKI et al. ) Examiner: M. Wilczewski  
Serial No. 08/691,434 )  
Filed: August 2, 1996 )  
For: METHOD OF FABRICATING )  
SEMICONDUCTOR DEVICES AND )  
APPARATUS FOR PROCESSING A )  
SEMICONDUCTOR )

**CERTIFICATE OF MAILING**

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*Done M. Wilczewski*

**LETTER RESUBMITTING PETITION TO WITHDRAW**  
**HOLDING OF ABANDONMENT**

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P.O. Box 1450  
Alexandria, Virginia 22313-1450

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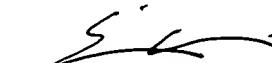
**MAR 08 2004**

Sir:

Applicant's representative contacted Examiner Wilczewski on March 1, 2004 regarding the status of the above-identified application. Examiner Wilczewski indicated that the *Petition to Withdraw Holding of Abandonment* filed by Applicant on June 25, 2003 was never received by the Patent Office. Enclosed herewith is a copy of the *Petition to Withdraw Holding of Abandonment*, a copy of the stamped receipt card evidencing receipt by the Patent Office on June 25, 2003. Also, enclosed are the documents attached to the *Petition to Withdraw Holding of Abandonment* including a copy of the formal drawings submitted by our draftsman, Quinn Patent Drawing Service, on February 25, 2003, along with the stamped acknowledgement from the Office of Patent Publication.

In view of the above and the enclosed documents herewith, it is believed that the *Notice of Abandonment* was issued in error. Therefore, it is respectfully requested that the *Notice of Abandonment* be rescinded.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285  
Robinson Intellectual Property Law Office, P.C.  
PMB 955  
21010 Southbank Street  
Potomac Falls, Virginia 20165

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1. Response to Notice of Abandonment (2 copies)
2. Copy of Notice of Abandonment
3. Copy of Formal Drawings and Stamped Receipt (2 copies)
4. Certificate of Mailing

In re PATENT application of:

SHUNPEI YAMAZAKI et al

Serial No. 08/691,434

Filed: 08/02/1996

Due Date: 08/11/2003

For: METHOD OF FABRICATING SEMICONDUCTOR DEVICES AND APPARATUS FOR  
PROCESSING A SEMICONDUCTOR

Docket: 0756-1551

June \_\_\_\_\_ 2003

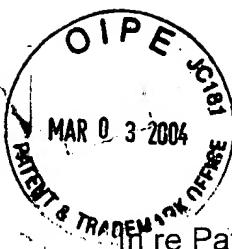
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Shunpei YAMAZAKI et al.

Serial No. 08/691,434

Filed: August 2, 1996

For: METHOD OF FABRICATING

SEMICONDUCTOR DEVICES AND

APPARATUS FOR PROCESSING A

SEMICONDUCTOR

) Art Unit: 2822

) Examiner: M. Wilczewski

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*Rosie M. Sieber*

**PETITION TO WITHDRAW HOLDING OF ABANDONMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Sir:

Applicant's representative received a *Notice of Abandonment* from the U.S. Patent Office mailed June 11, 2003 in the above-identified application. The *Notice of Abandonment* was issued due to "Applicant's failure to timely file new formal drawings as required in the Notice of Allowability."

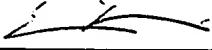
In response to the *Notice of Allowability* dated January 28, 2003, formal drawings were submitted by our draftsman, Quinn Patent Drawing Service, on February 25, 2003. Enclosed please find copies of the formal drawings along with the stamped acknowledgement from the Office of Patent Publication.

In view of the above and the enclosed documents herewith, it is believed that the *Notice of Abandonment* was issued in error. Therefore, it is respectfully requested that the *Notice of Abandonment* be rescinded.

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Respectfully submitted,

**MAR 08 2004**

  
Eric J. Robinson

Reg. No. 38,285

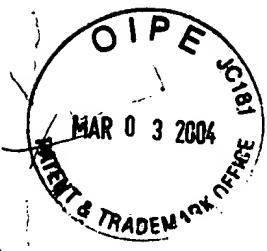
Robinson Intellectual Property Law Office, P.C.

PMB 955

21010 Southbank Street

Potomac Falls, Virginia 20165

(571) 434-6789



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(Please complete entire form)

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You are hereby authorized to correct the drawing(s) in the case indicated below:

- Correct informalities cited by Chief Draftsman (Include copy of Form #948)
- Enter changes approved by the Examiner (Include prints with changes marked in red)
- When double-line hatching or criss-cross hatching cited - advise correct hatching.

Special Instructions:

- Forward the formal drawings and file copies.
- OR
- Forward a copy of changed drawings and notification of date resubmitted in USPTO to address below.
- A copy of the drawings as presently on file are enclosed for your use in making the required corrections.

ATTACH THE NOTICE OF ALLOWABILITY OR NOTICE OF ALLOWANCE

Name: Robinson Intellectual Property Law Office, P.C.  
Address: PMB 955  
21010 Southbank Street  
Potomac Falls, Virginia 20165  
Phone No. (571) 434-6789

Docket: 0756-1551

Date: 02/03/03

Corrected drawings due in USPTO no later than 04/28/03

\*\*\*\*\*

NEW DRAWINGS

Please allow Quinn Patent Drawing Service to file the attached new drawing sheets for the ones originally filed.

Applicant Shunpei YAMAZAKI; Yasuhiko TAKEMURA; Akira TAKENOUCHI

Serial No. 08/691,424 Filing Date 08/02/96

Title: Method of Fabricating Semiconductor Devices and Apparatus for Processing a Semiconductor

Date Allowed 01/28/03 Conf. No. 2240

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FEB 25 2003

  
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Office of Patent Publication  
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FIG. 1

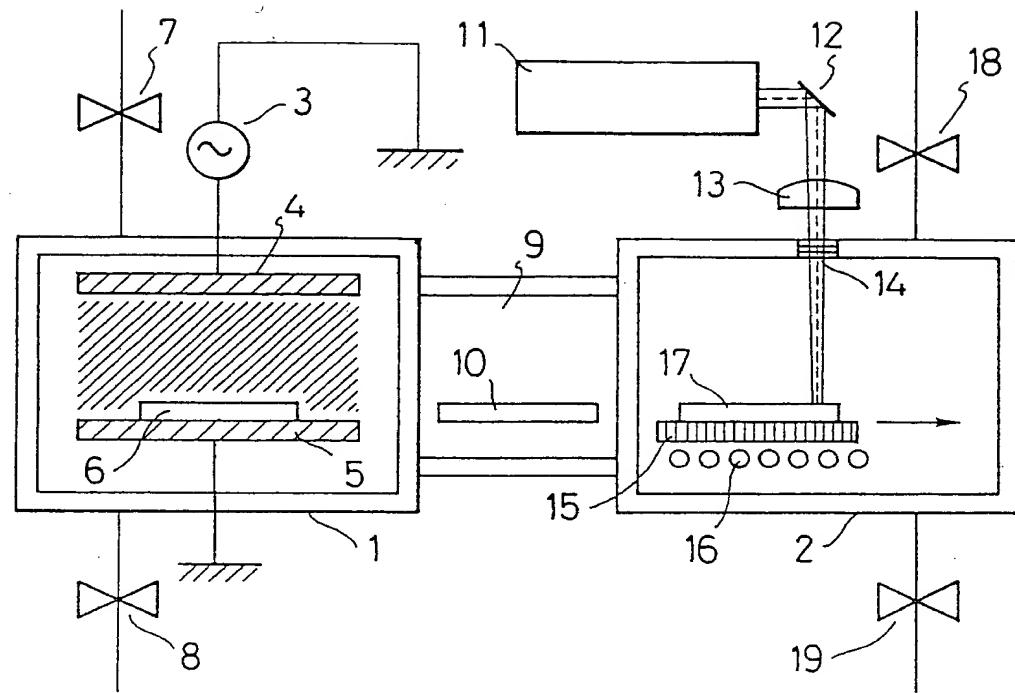
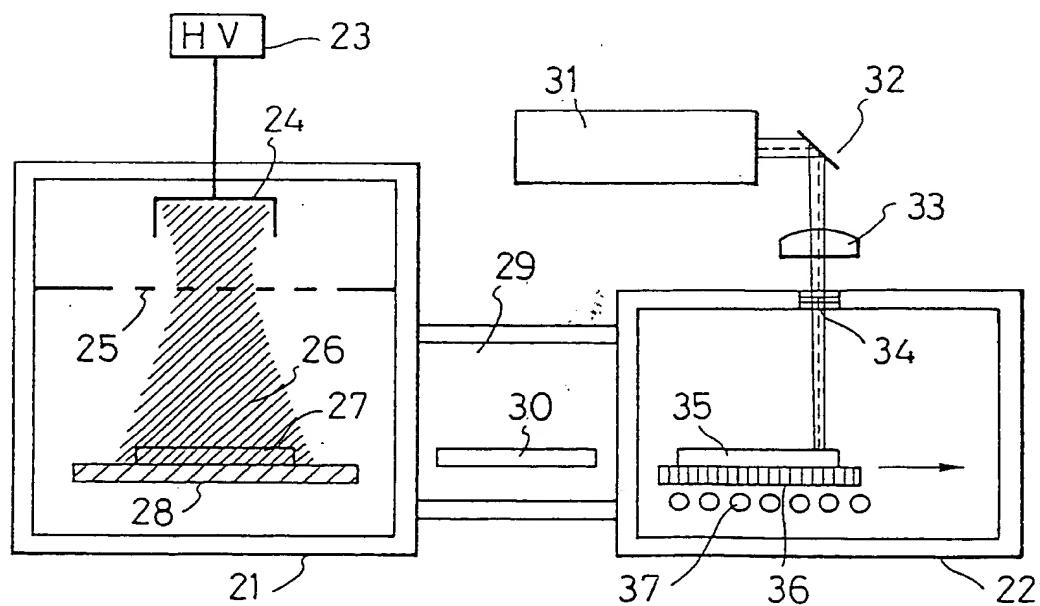


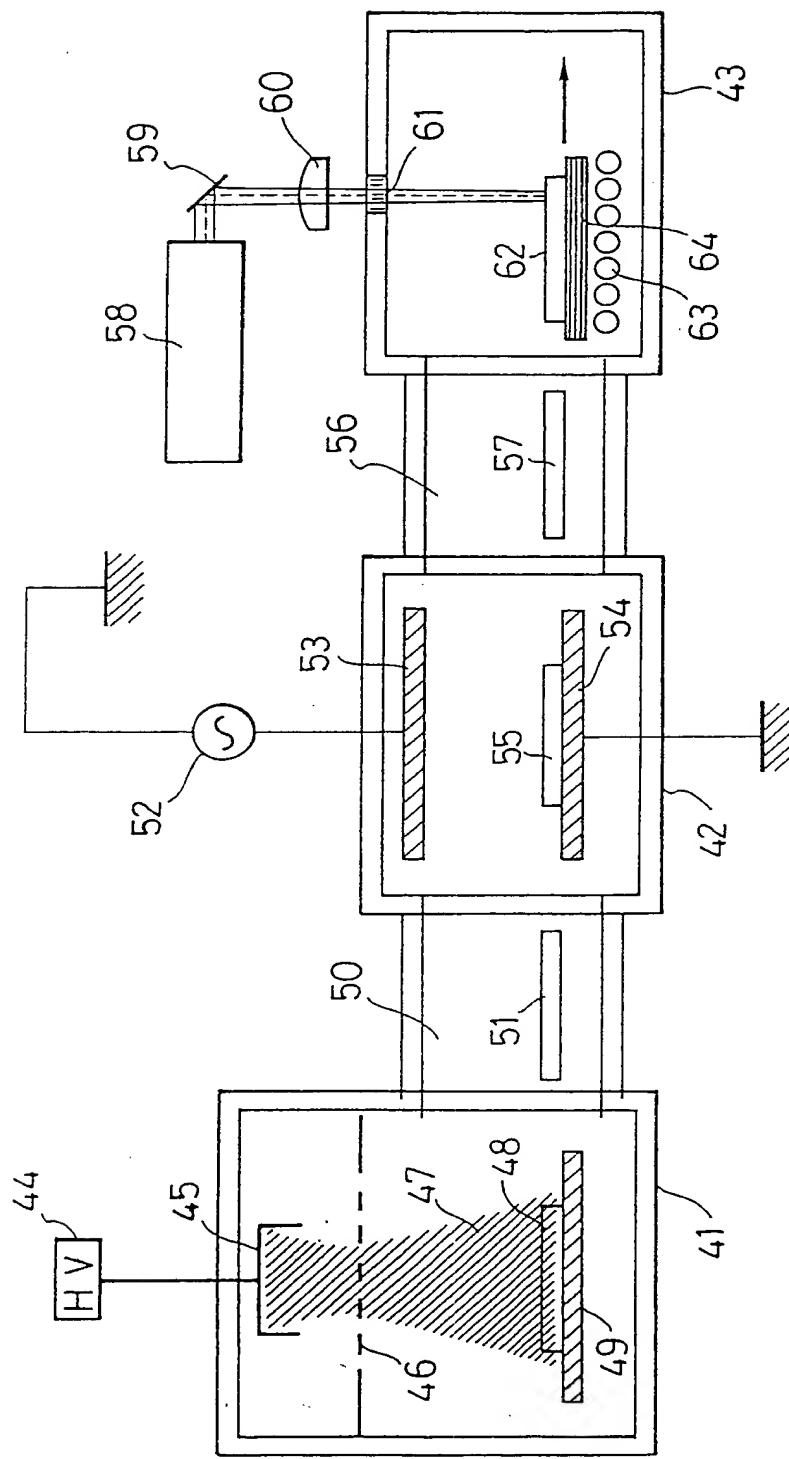
FIG. 2





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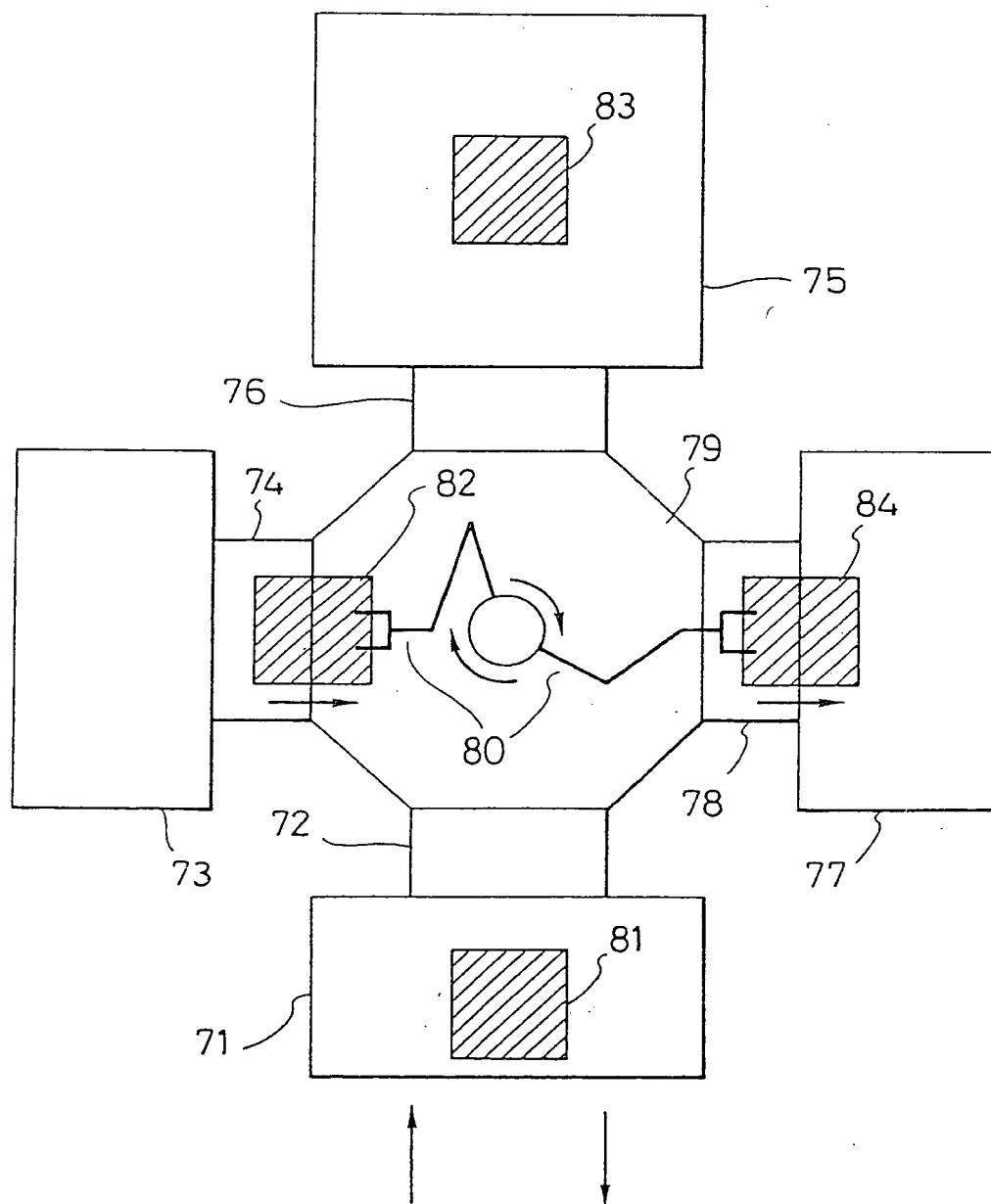
FIG. 3





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FIG. 4





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FIG. 5(A)

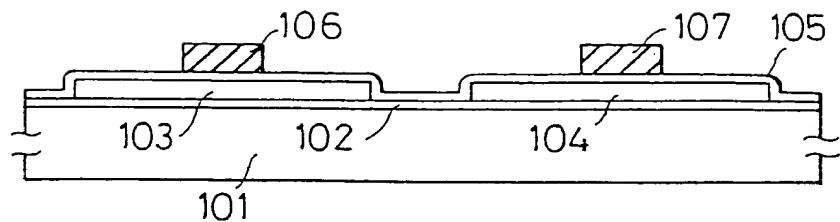


FIG. 5(B)

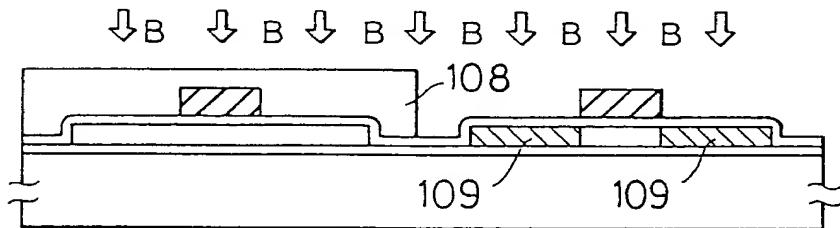


FIG. 5(C)

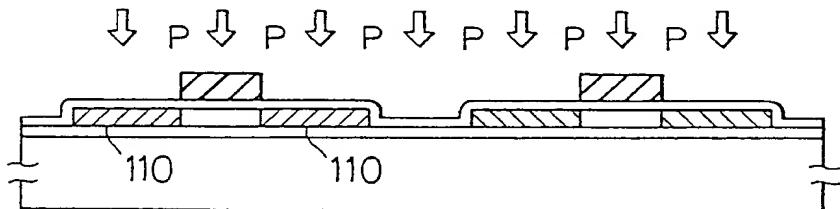


FIG. 5(D)

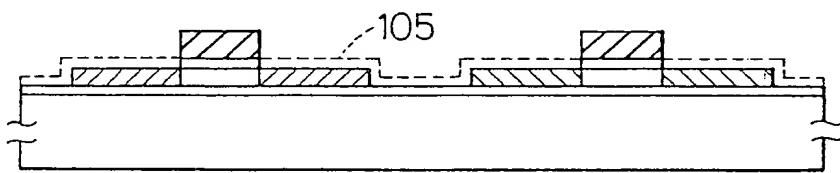


FIG. 5(E)

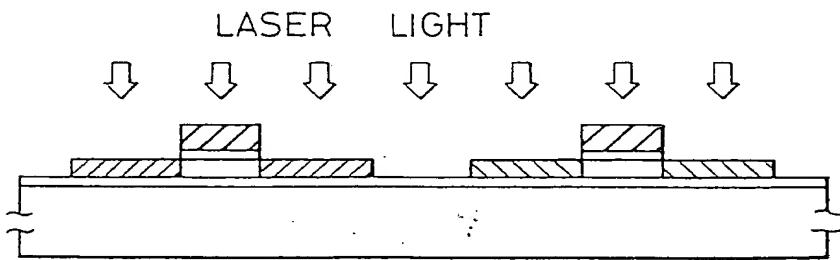
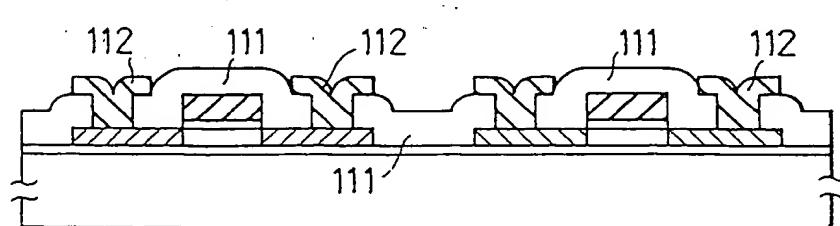


FIG. 5(F)





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FIG. 6

